

L Number	Hits	Search Text	DB	Time stamp
1	452	(((((first and second) (upper and lower)(top and bottom)(counter oppos\$4))adj3 (substrate\$2 support board)) near51(adhesive groove\$2 trench\$3))same(interpos\$4 inject\$4))	USPAT; US-PGPUB	2003/10/06 17:03
2	1	(tft lcd thin adj film adj transistor\$2 display\$2)and(solvent\$1free) and ((((((first and second) (upper and lower)(top and bottom)(counter oppos\$4))adj3 (substrate\$2 support board)) near51(adhesive groove\$2 trench\$3))same(interpos\$4 inject\$4)))	USPAT; US-PGPUB	2003/10/06 17:03
3	34	(tft lcd thin adj film adj transistor\$2 display\$2)and(bubble\$2 crack\$2) and ((((((first and second) (upper and lower)(top and bottom)(counter oppos\$4))adj3 (substrate\$2 support board)) near51(adhesive groove\$2 trench\$3))same(interpos\$4 inject\$4)))	USPAT; US-PGPUB	2003/10/06 17:03
4	3	((((plastic flexible)near17(rigid glass))adj3 (substrate\$2)) and (tft lcd)and (solvent\$1free)	USPAT; US-PGPUB	2003/10/06 17:09
5	165	((((plastic flexible)near17(rigid glass))adj3 (substrate\$2)) and (tft lcd)and (bubble\$2 crack\$2)	USPAT; US-PGPUB	2003/10/06 17:10
6	1	((((plastic flexible)near17(rigid glass))adj3 (substrate\$2)) and (tft lcd)and (bubble\$2 crack\$2)) and ((adhesive)near31(groove\$2 trench\$3))	USPAT; US-PGPUB	2003/10/06 16:58
7	66	((((plastic flexible)near17(rigid glass))adj3 (substrate\$2)) and (tft lcd)and (bubble\$2 crack\$2)) and ((groove\$2 trench\$3))	USPAT; US-PGPUB	2003/10/06 17:00
8	22	((opening\$2 through\$2 hole\$2)near4(adhesiv\$4))and (((((plastic flexible)near17(rigid glass))adj3 (substrate\$2)) and (tft lcd)and (bubble\$2 crack\$2))	USPAT; US-PGPUB	2003/10/06 17:01
9	269	(((((first and second) (upper and lower)(top and bottom)(counter oppos\$4))adj3 (substrate\$2 support board)) near51(adhesive groove\$2 trench\$3))and(interpos\$4 inject\$4))	EPO; JPO; DERWENT	2003/10/06 17:03
10	0	(tft lcd thin adj film adj transistor\$2 display\$2)and(solvent\$1free) and ((((((first and second) (upper and lower)(top and bottom)(counter oppos\$4))adj3 (substrate\$2 support board)) near51(adhesive groove\$2 trench\$3))and(interpos\$4 inject\$4)))	EPO; JPO; DERWENT	2003/10/06 17:03
11	8	(tft lcd thin adj film adj transistor\$2 display\$2)and(bubble\$2 crack\$2) and ((((((first and second) (upper and lower)(top and bottom)(counter oppos\$4))adj3 (substrate\$2 support board)) near51(adhesive groove\$2 trench\$3))and(interpos\$4 inject\$4)))	EPO; JPO; DERWENT	2003/10/06 17:04
12	0	((((plastic\$7 flexi\$7)near17(rigid\$7 glass))adj3 (substrate\$2)) and (tft lcd)and (solvent\$1free)	EPO; JPO; DERWENT	2003/10/06 17:09
13	0	((((plastic\$7 flexi\$7)near17(rigid\$7 glass))adj3 (substrate\$2)) and (tft lcd)and (bubble\$2 crack\$2)	EPO; JPO; DERWENT	2003/10/06 17:10
14	5	((((plastic\$7 flexi\$7)and(rigid\$7 glass))adj3 (substrate\$2)) and (tft lcd)and (bubble\$2 crack\$2)	EPO; JPO; DERWENT	2003/10/06 17:15
15	1	((((plastic\$7 flexi\$7)and(rigid\$7 glass))adj3 (substrate\$2)) and (tft lcd)and ((groove\$2 trench\$3))	EPO; JPO; DERWENT	2003/10/06 17:16
16	3	((((plastic\$7 flexi\$7)and(rigid\$7 glass))adj3 (substrate\$2)) and (tft lcd thin adj film adj transistor\$2 display\$2)and ((groove\$2 trench\$3))	EPO; JPO; DERWENT	2003/10/06 17:16